## **SANYO Semiconductors**



## **APPLICATION NOTE**

An ON Semiconductor Company

# LV8548M

## **Bi-CMOS** integrated circuit

# 12V Low Saturation Voltage Drive Forward/Reverse Motor Driver

#### Overview

The LV8548M is a 2-channel low saturation voltage forward/reverse motor driver IC. It is optimal for motor drive in 12V system products and can drive either two DC motors, one DC motor using parallel connection, or it can drive a stepping motor in Full-step and Half-step.

#### **Functions**

- DMOS output transistor adoption (Upper and lower total RON =  $1\Omega typ$ )
- For one power supply (The control system power supply is unnecessary.)
- Our motor driver IC, LB1948M, and compatible pin
- It is possible to connect it in parallel (parallel, connected operation of drive ch).

OUT1

OUT2

- The compact package (MFP-10S) is adopted.
- V<sub>CC</sub> max = 20v, I<sub>O</sub> max = 1A
- Current consumption 0 when standing by
- Built-in brake function

## **Typical Applications**

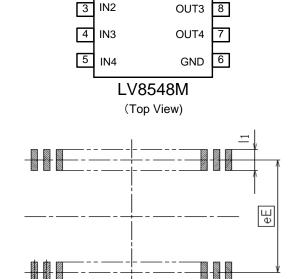
- Refrigerators
- Time Recorder
- Label Printer

- Vacuum cleaner
- POS Printer
- TOY

### **Pin Assignment**

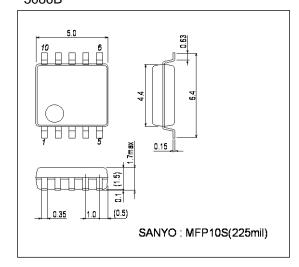
VCC

IN1



#### **Package Dimensions**

unit : mm (typ) 3086B



(Unit:mm)

	` ` `			
Reference Symbol	MFP10S (225mil)			
еE	5.70			
е	1.00			
b3	0.47			
I1	1.10			

Caution: The package dimension is a reference value, which is not a guaranteed value.

bз

## **Block Diagram**

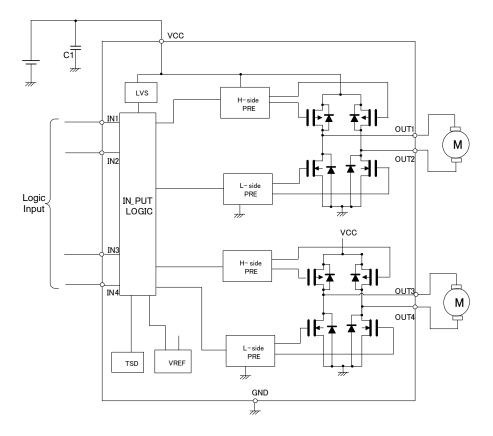


Figure1 Two DC motor drive

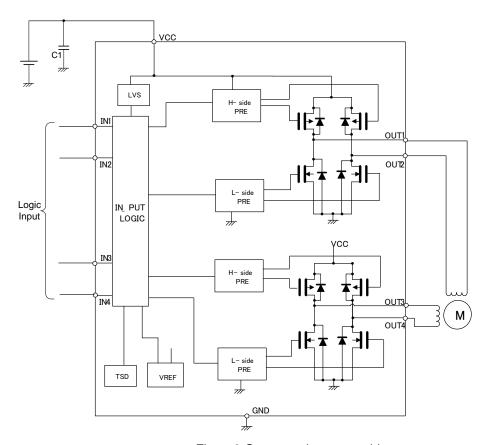


Figure 2 One stepping motor drive

## **Specifications**

## Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum power supply voltage	V <sub>CC</sub> max		-0.3 to +20	V
Output impression voltage	VOUT		-0.3 to +20	V
Input impression voltage	VIN		-0.3 to +6	V
GND pin outflow current	IGND	For ch	1.0	Α
Allowable Power dissipation	Pd max	*	1.05	W
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

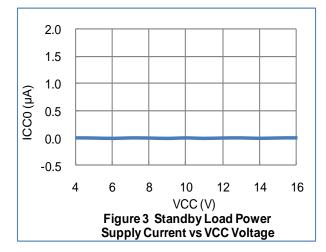
<sup>\*:</sup> When mounted on the specified printed circuit board (57.0mm x57.0mm x 1.6mm), glass epoxy, both sides

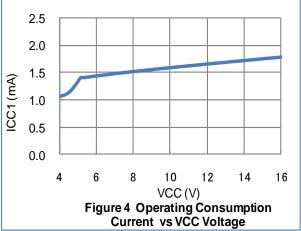
## **Recommended Operating Range** at $Ta = 25^{\circ}C$

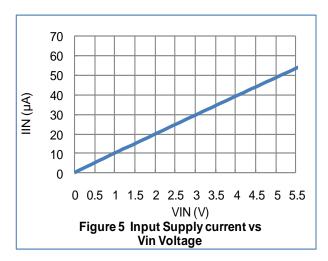
Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	VCC		4.0 to 16	V
Input "H" level voltage	V <sub>IN</sub> H		+1.8 to +5.5	V
Input "L" level voltage	VINL		-0.3 to +0.7	V

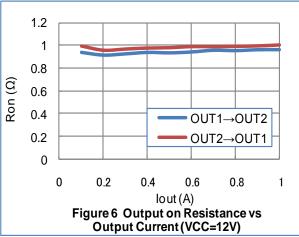
## **Electrical Characteristics** at Ta = 25°C, $V_{CC}$ = 12V

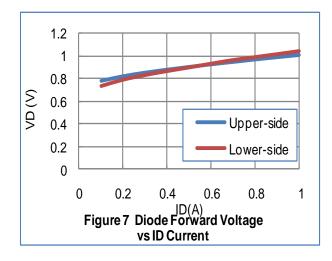
Parameter	Symbol	Conditions		Unit			
Farametel Symbo		Conditions		typ	max	Offic	
Power supply current	ICC0	Standby mode IN1 = IN2 = IN3 = IN4 = "LOW"			1	μА	
	ICC1	It is "High" from IN1 as for either of IN4. Load opening		1.7	2.3	mA	
Input current	IIN	$V_{IN} = 5V$	35	50	65	μΑ	
Thermal shutdown operating temperature	Ttsd	Design certification	150	180	210	°C	
Width of temperature hysteria	∆Ttsd	Design certification		40		°C	
Low voltage protection function operation voltage	VthVCC		3.3	3.5	3.65	V	
Release voltage	Vthret		3.55	3.8	3.95	V	
Output ON resistance (Upper and lower total)	RON	I <sub>OUT</sub> = 1.0A	0.7	1	1.25	Ω	
Output leak current	lOleak	V <sub>O</sub> = 16V			10	μΑ	
Diode forward voltage	VD	ID = 1.0A		1.0	1.2	V	

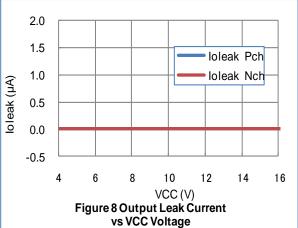


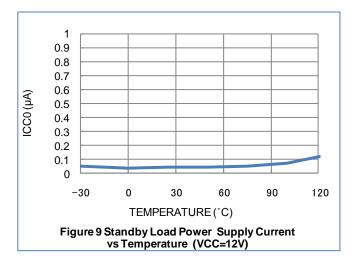


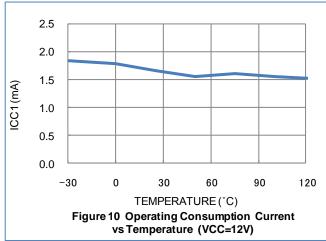


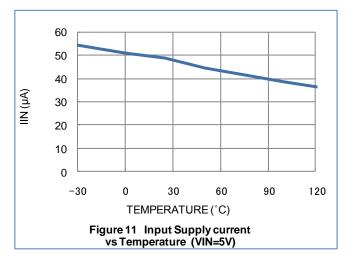


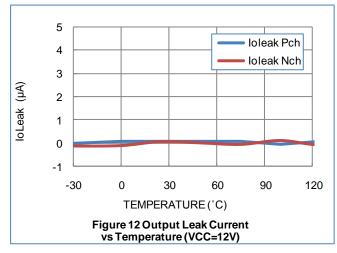


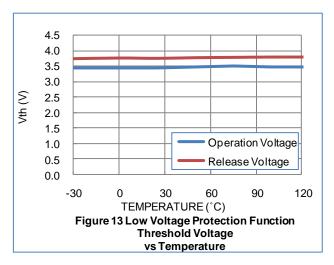


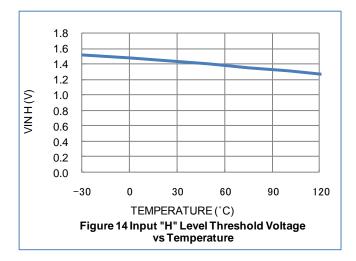


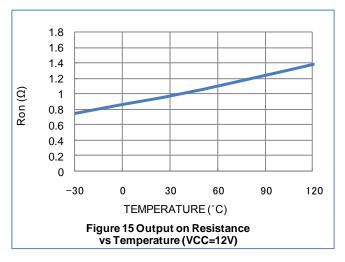


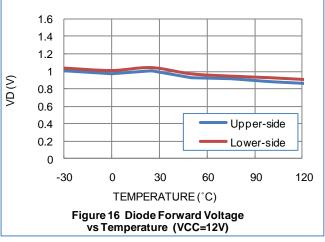












## Pin function

Pin No.	Pin name	Pin function	Equivalent Circuit
1	Vcc	Power-supply voltage pin.  VCC voltage is impressed. The permissible operation voltage is from 4.0 to 16.0(V). The capacitor is connected for stabilization for GND pin (6pin).	
2	IN1	Motor drive control input pin. Driving control input pin of OUT1 (10pin) and OUT2 (9pin). It combines with IN2 pin (3pin) and it fights desperately. The digital input it, range of the "L" level input is 0 to 0.7(V), range of the "H" level input is from 1.8 to 5.5(V). PWM can be input. Pull-down resistance $100(k\Omega)$ is built into in the pin. It becomes a standby mode because all IN1, IN2, IN3, and IN4 pins are made "L", and the circuit current can be adjusted to 0.	0 + W - + 40KΩ -
3	IN2	Motor drive control input pin. Driving control input pin of OUT1 (10pin) and OUT2 (9pin). It combines with IN1 pin (2pin) and it uses it. PWM can be input. With built-in pull-down resistance.	100ΚΩ
4	IN3	Motor drive control input pin.  Driving control input pin of OUT3 (8pin) and OUT4 (7pin). It combines with IN4 pin (5pin) and it uses it. PWM can be input. With built-in pull-down resistance.	<i>""</i>
5	IN4	Motor drive control input pin.  Driving control input pin of OUT3 (8pin) and OUT4 (7pin). It combines with IN3 pin (4pin) and it uses it. PWM can be input. With built-in pull-down resistance.	
6	GND	Ground pin.	
7	OUT4	Driving output pin. The motor coil is connected between terminal OUT3 (8pin).	vcc
8	OUT3	Driving output pin. The motor coil is connected between terminal OUT4 (7pin).	OUT1 (OUT3) OUT2 (OUT4)
9	OUT2	Driving output pin. The motor coil is connected between terminal OUT1 (10pin).	- (0014) 
10	OUT1	Driving output pin. The motor coil is connected between terminal OUT2 (9pin).	GND 711

#### Operation explanation

#### 1. DCM output control logic

	Input			Output				-	Remarks		
IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4	remans			
L	L	L	L	OFF	OFF	OFF	OFF	Stand-by			
L	L			OFF	OFF				Stand-by		
Н	L			Н	L			1CH	Forward		
L	Н			L	Н			1011	Reverse		
Н	Н			L	L				Brake		
		L	L			OFF	OFF		Stand-by		
		Н	L			H L		2CH	Forward		
		L	Н	] [		L H		ZCH	Reverse		
		Н	Н			L	L		Brake		

#### 2. About the switch time from the stand-by to the operation

When IN1, IN2, IN3, IN4 are "L", this IC has completely stopped operating. After the time of reset (about  $7\mu s$  of an internal setting) it shifts to a prescribed output status corresponding to the state of the input when the signal enters the input terminal.

Reset of about  $7\mu s$  doesn't hang even if the motor is driven from the stand-by state when either CH drives and the output becomes an output status corresponding to the state of the input. As for full power TR between the reset time, turning off is maintained.

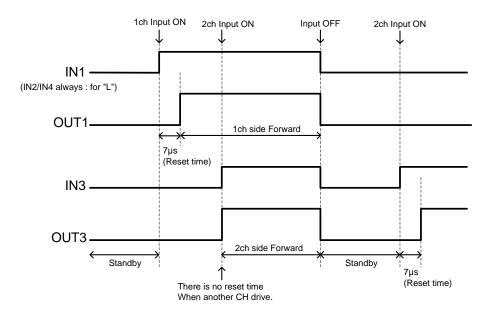


Figure 17 switch time from the stand-by to the operation

#### 3. Thermal shutdown function

The thermal shutdown circuit is incorporated and the output is turned off when junction temperature Tj exceeds 180°C. As the temperature falls by hysteresis, the output turned on again (automatic restoration). The thermal shutdown circuit does not guarantee the protection of the final product because it operates when the temperature exceed the junction temperature of Tjmax=150°C.

TSD = 
$$180^{\circ}$$
C (typ)  
 $\Delta$ TSD =  $40^{\circ}$ C (typ)

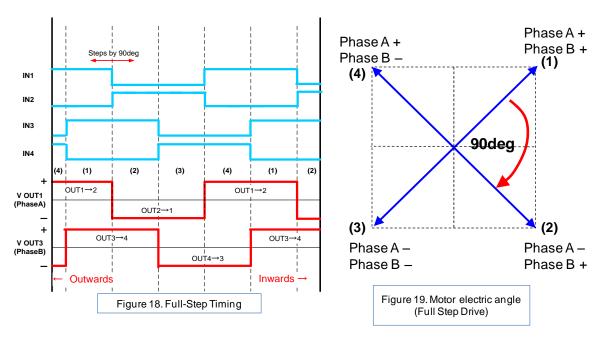
#### 4. Low voltage protection function

When the power supply voltage is as follows typical 3.5V, the output does OFF.

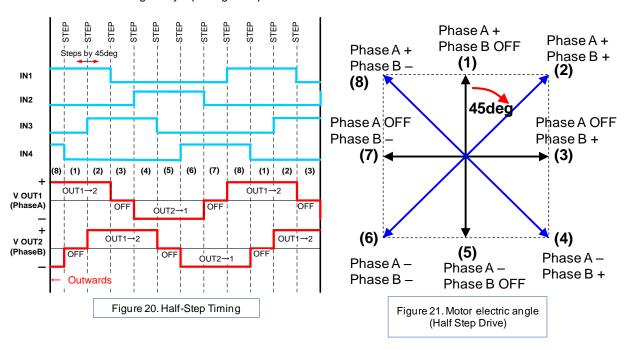
When the power supply voltage is as above typical 3.8V, the IC outputs a set state.

## Operation principal

 Full-Step Drive Motor advances 90 degree by inputting 1 step.

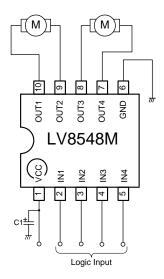


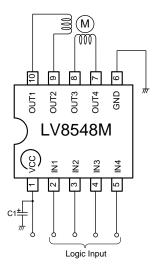
• Half-Step Drive Motor advances 45 degree by inputting 1 step.



## **Application Circuit Example**

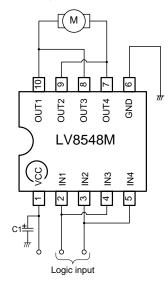
- 1. Example of applied circuit when two DC motor driving
- 2. Example of applied circuit when one stepping motor driving





3. Example of applied circuit when connecting it in parallel

The use likened to H-Bridge 1ch is shown possible in the figure below by connecting IN1 with IN3, IN2 with IN4, OUT1 with OUT3, and OUT2 with OUT4. ( $I_{O}$  max = 2.0A, Upper and lower total  $R_{ON}$  = 0.5 $\Omega$ )



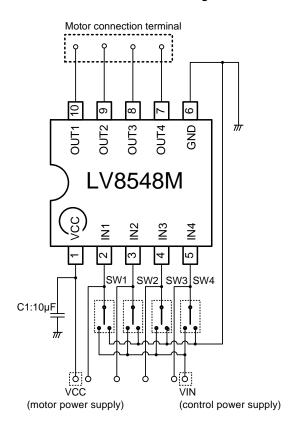
\* Bypass capacitor (C1) connected between  $V_{CC}$ -GND of all examples of applied circuit recommends the electric field capacitor of  $0.1\mu A$  to  $10\mu A$ .

Confirm there is no problem in operation in the state of the motor load including the temperature property about the value of the capacitor.

Mount the position where the capacitor is mounted on nearest IC.

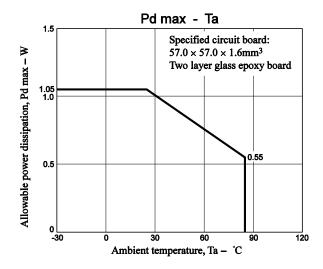
## **Evaluation Board Manual**

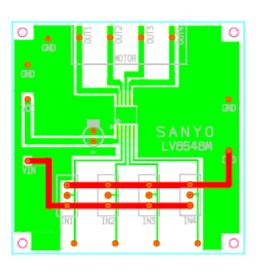
#### 1. Evaluation Board circuit diagram



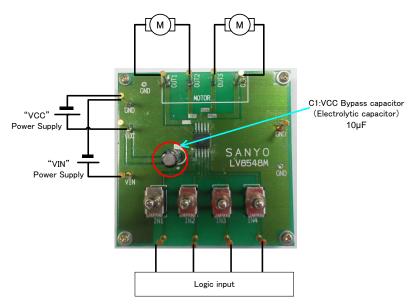
Bill of Materials for LV8548M Evaluation Board

Designator	Qty	Description	Value	Tol	Footprint	Manufacturer	Manufacturer Part Number	Substitution Allowed	Lead Free
IC1	1	Motor Driver			MFP10S (225mil)	SANYO semiconductor	LV8548M	No	Yes
C1	1	VCC Bypass capacitor	10μF 50V	±20%		SUN Electronic Industries	50ME10HC	Yes	Yes
SW1-SW4	4	Switch				MIYAMA	MS-621-A01	Yes	Yes
TP1-TP12	12	Test points				MAC8	ST-1-3	Yes	Yes





#### 2. Two DC motor drive



- Connect OUT1 and OUT2, OUT3 and OUT4 to a DC motor each.
- Connect the motor power supply with the terminal VCC, the control power supply with the terminal VIN. Connect the GND line with the terminal GND.
- DC motor becomes the predetermined output state corresponding to the input state by inputting a signal such as the following truth value table into IN1~IN4.
- •See the table in p.8 for further information on input logic.

When you drive DC motor with LV8548M, caution is required to switch motor rotation from forward to reverse because when doing so, electromotive force (EMF) is generated and in some cases, current can exceed the ratings which may lead to the destruction and malfunction of the IC .

Coil current (lout) for each operation is obtained as follows when switching motor rotation from forward to reverse.

#### • Starting up motor operation

Coil current lout = ( VCC - EMF ) / coil resistance

At startup, lout is high because EMF is 0. As the motor starts to rotate, EMF becomes higher and lout becomes lower.

• When switching motor rotation from forward to reverse:

Coil current lout = ( VCC + EMF ) / coil resistance

When EMF is nearly equal to VCC at a max, make sure that the current does not exceed lomax since a current which is about double the startup current may flow at reverse brake.

#### • Short brake:

Coil current: lout = EMF / coil resistance

Since EMF is 0 when the rotation of motor stops, lout is 0 as well.

When you switch motor rotation form forward to reverse, if lout is higher than lomax, you can operate short brake mode between forward and reverse either to slow down or stop the motor.

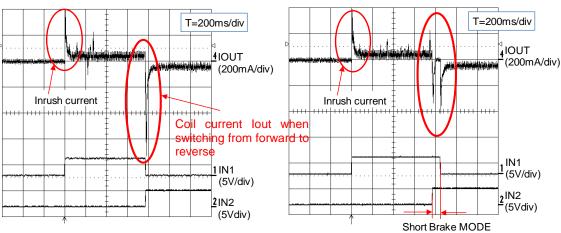


Figure 22 Without Break MODE

Figure 23 With Break MODE

#### Input and output characteristics of H-Bridge

LV8548M can be driven by direct PWM control of H-Bridge by inputting PWM signal to IN.

However output response of H-Bridge worsens around On-duty 0%, which generates dead zone. As a result, IC control loses lineality.

If you intend to drive motor in such control range, make sure to check the operation of your motor.

Input-Output Characteristics of H-Bridge (reference data) Forward/Reverse⇔Brake

Vcc=12V

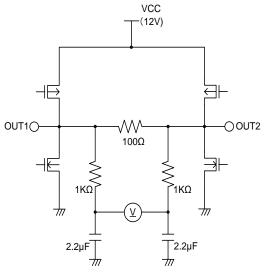
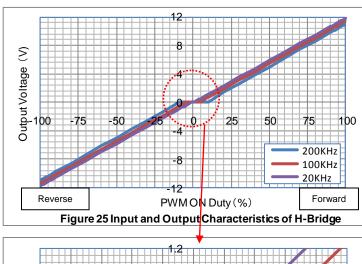
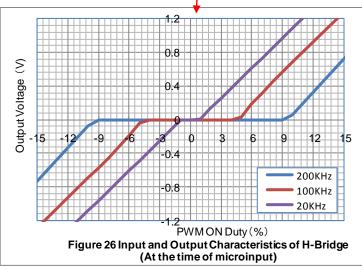
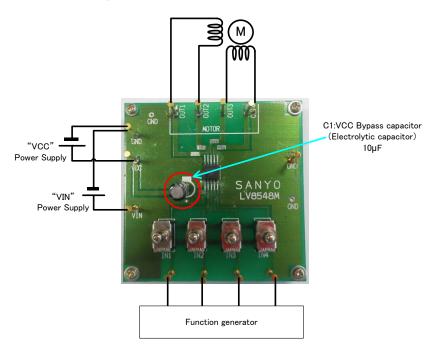


Figure 24 Measurement connection diagram





#### 3. One stepping motor drive

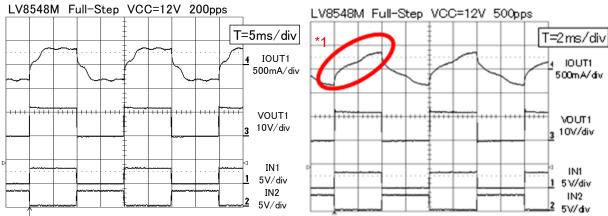


- Connect a stepping motor with OUT1, OUT2, OUT3 and OUT4.
- Connect the motor power supply with the terminal VCC, the control power supply with the terminal VIN. Connect the GND line with the terminal GND.
- STP motor drives it in a Full-Step, Half-Step by inputting a signal such as follows into IN1~IN4.
- For input signal to function generator, refer to p.9.

  To reverse motor rotation, make sure to input signal to outward direction.

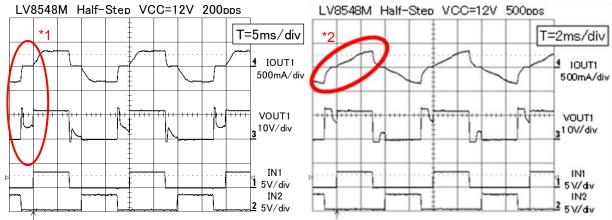
#### Waveform of LV8548M evaluation board when driving stepping motor

• Full-Step Drive



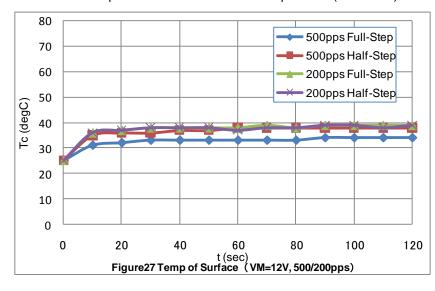
<sup>\*1.</sup> When the motor rotation is at a high speed, current gradient increases by the inductance of motor (L).

#### •Half-Step Drive



- \*1.With Half-Step mode, voltage kick-back and electromotive force occur in current OFF period.
- \*2. When the motor rotation is at a high speed, current gradient increases by inductance of motor (L).

#### •IC surface temperature when a motor is in operation (reference)





【Stepping motor driven by LV8548M 】 Motor diameter: 20.5mm

Coil resistance: 30.8Ω

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